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UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **KIRA, et al.**

Group Art Unit: 2652

Serial No.: **09/548,313**

Examiner: **Craig A. Renner**

Filed: **April 12, 2000**

P.T.O. Confirmation No.: 6169

For: **HEAD ASSEMBLY HAVING INTEGRATED CIRCUIT CHIP COVERED BY LAYER WHICH PREVENTS FOREIGN PARTICLE GENERATION**

RESPONSE UNDER 37 CFR §1.116
- EXPEDITED RESPONSE -
GROUP ART UNIT 2652

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JUN 14 2004

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Technology Center 2600

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 8, 2004

Sir:

In response to the Office Action dated **April 23, 2004**, please amend the above-identified application as follows:

V
Do Not Enter
AH/6-16-04